Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	(Mauricio with Calle).in. and agere. as. and first with portion and second with portion	USPAT	OR	OFF	2005/09/08 15:30
S2	3	("5720032" | "6115394" | "6754200").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/09/08 15:16
S3	38	processor and packet near2 analyzer and memory and external with memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 15:36
S4	14	processor same packet near2 analyzer and memory and external with memory	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 15:36
S5	13	processor same packet near2 analyzer and memory and external with memory and portion	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 20:38
S6	3641	370/392.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 20:38
S7	1258	370/474.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 20:39
S8	4800	S6 or S7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 20:39
S9	4800	S6 or S7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 20:39

S10	3641	S6 or S7 and processor and packet adj analyer and memory and ((second external) near3 memory) and ethernet and gigabit with switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 20:42
S11	0	S9 and processor and packet adj analyer and memory and ((second external) near3 memory) and ethernet and gigabit with switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 20:45
S17	0	S9 and processor and packet adj analyzer and memory and ((second external) near3 memory) and ethernet and gigabit with switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 20:45
S18	2	S9 and processor and packet adj analyzer and memory and ((second external) near3 memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 20:46
S19	19	processor and packet adj analyzer and memory and ((second external) near3 memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 20:52
S20	3	processor and packet adj analyzer and memory and ((second external) near3 memory) and ethernet and (gigabit Gbit Gb) and switch	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 21:13
S21	3	processor and packet adj analyzer and memory and ((second external) near3 memory) and ethernet and (gigabit Gbit Gb) and switch and portion	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/08 21:17
S22	3	processor and packet adj analyzer and memory and ((second external) near3 memory) and ethernet and (gigabit Gbit Gb) and switch and portion and packet	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/09 00:49

| S23 | 2 | processor and packet adj analyzer and memory and ((second external) near3 memory) and ethernet and (gigabit Gbit Gb) and switch and portion and packet and table | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/09 00:49 |